



## **TECHNICAL DATASHEET**

## Summary

The iMAC LED4 Module is an Intrinsically Safe LED Output Module that provides four (4) general purpose LED outputs. The module is powered directly from the iMAC L1 Fieldbus

When enabled, the output LEDs flash at 1.2Hz with sufficient intensity to be visible in bright ambient conditions.

The LED4 module is an output device and as such, its address can be set to the same address as an input device, such as a DI4, to indicate input states. Multiple LED4 modules may be programmed to the same address, allowing distributed simultaneous LED indication in multiple locations.

Typical applications include remote isolation indication, indication of conveyor signal line status and remote indication

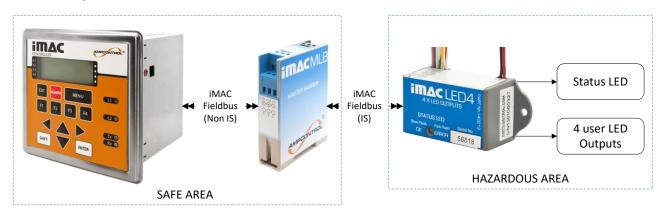


1 (Output)

### **Features**

- Intrinsically Safe IECEx Ex ia Group I Ma
- 4 user definable LED outputs
- Compact, encapsulated design
- Down-line powered from the iMAC L1 interface
- Multifunction diagnostic status LED
- Remotely controlled and configured via the iMAC Controller
- Optional DIN rail mounting kits are available

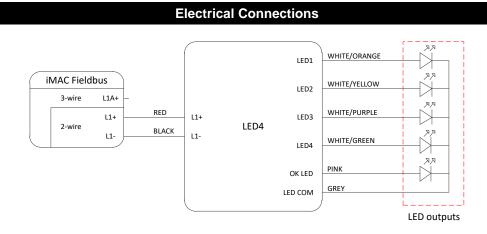
### Minimum System



## **CAUTION!**



Modules used in non-I.S. systems shall not be re-used in I.S. systems (as the integrity of internal components upon which intrinsic safety depends may have been compromised).



Note: refer to iMACB094 - iMAC Installation Requirements

Label	Wire colour	Туре	Description
L1+	Red	I 1 Commo	iMAC Fieldbus (2 wire)
L1-	Black	L1 Comms	
LED1	White/Orange		Hear (4)
LED2	White/Yellow	I ED sutmits	
LED3	White/Purple		` '
LED4	White/Green	LED outputs	
OK LED	Pink		Status / Ok
LED COM	Grey		Common (cathodes)

# Data Register(s)

Output Register				
Bit	Description	Bit Value	R/W	Invert Bit
15	-	X	W	-
14	-	X	W	=
13	-	X	W	=
12	-	Χ	W	-
11	-	Χ	W	-
10	-	X	W	=
9	-	X	W	=
8	-	Χ	W	-
7	-	Χ	W	-
6	-	Х	W	-
5	-	X	W	-
4	-	X	W	-
3	LED4	1 = On (flash)	W	3 (0008h)
2	LED3	1 = On (flash)	W	2 (0004h)
1	LED2	1 = On (flash)	W	1 (0002h)
0	LED1	1 = On (flash)	W	0 (0001h)

# **Configuration Parameters**

(Refer to document IMACB005 - iMAC module parameters programming procedure)

	Output Register Parameters (roll-call name: LED4 Module)				
No	Description	Range	Default	Units	R/W
1	Output register address	1 - 255	255	-	r/w
2	Output register LEDx bits invert	0000h – 000Fh	0000h	-	r/w
3	Not used	-	-	-	r
4	Not used (Factory use)	-	-	-	r

## **IMACB060 LED4 TECHNICAL DATASHEET** Version: 1, Date: 21 APRIL 2020

### Parameter Details...

Parameter 2: Invert bits - specify whether received output data bits set the LED/s to ON or OFF. If invert bit is 0, then corresponding LED Output is ON when received data bit is a 1 and OFF when a 0. Alternatively, if invert bit is 1, then corresponding LED Output is ON when received data bit is 0 and OFF when a 1.

Input Register Status			
Output register – LEDx bit value	Output register parameter – invert bit value	LED output	
0	0	OFF	
1	0	ON	
0	1	ON	
1	1	OFF	

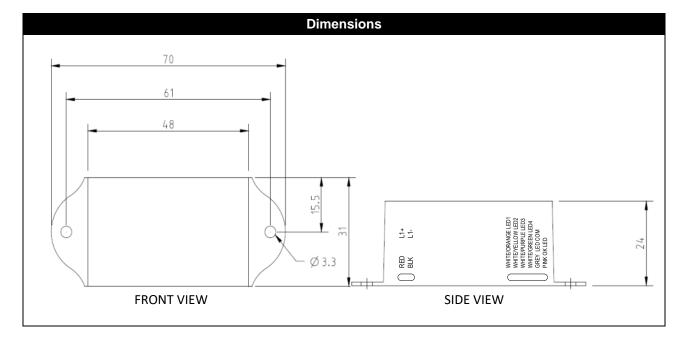
LED Indicators				
Status LED	Status LED			
	Sequence	Module – iMAC Comms Status	Module – Function Status	
Off	-	Unknown (check connections)	Unknown (check connections)	
Slow Flash	₹ 0 0 0 <del>-</del>	Healthy	All output register LEDx bits = 0	
2 Flashes	<b>(************************************</b>	Healthy (has been roll-called)	-	
3 Flashes	<b>一类 菜 菜 。</b>	Error (address clash)	-	
Fast Flash		Warn (general)	Any output register LEDx bit = 1	
Output LED1 to LED4				
Off	Output register LEDx bit = 0 (with Invert bit =0)			
Flash (1.2Hz)	Flash (1.2Hz) Output register LEDx bit = 1 (with Invert bit =0)			

	Certification / Approvals		
Туре	Ex ia I Ma (for use in zone 0, 1 or 2)		
Certificate number	IECEx ITA 07.0017X		
Module type	Module type GM1		
IP rating	Must be installed in an enclosure not I	ess than IP20 (IP54 recommended)	
Other	Must be mounted in such a manner that the encapsulation is not exposed Must be connected in accordance with iMAC system drawing IMACZ032. L1+ L1- terminals must only connect to a single MLB (Master Line Barrier).		
	L1+ (red), L1- (black)	Ui = 21.5V (44.65R source resistor) Ci = Negligible Li = Negligible	
I/O parameters	LED1(white/orange), LED2 (white/yellow), LED3 (white/purple), LED4 (white/green), OK LED (pink), LED COM (grey)	Uo = 21.5V lo = 0.202A Po = 1.09W Co = 0.27uF Lo = 11.4mH Ci = 5.83uF Li = negligible	
Ambient temperature (Ta) -20°C to +40°C (refer to operating environment specifications)			
This table is provided for quick reference purposes only: refer to latest issue of the Certificate of Conformity for all system designs.			

## IMACB060 LED4 TECHNICAL DATASHEET Version: 1, Date: 21 APRIL 2020

Specifications			
Mechanical			
Dimensions	31mm x 70mm x 24mm (See diagram below)		
Weight	60g		
IP Rating	Module is fully encapsulated		
Mounting	Enclosure includes 2 mounting tabs, each with a 3mm hole (screws not supplied)		
Electrical Connections	Individual 450mm flying leads (0.4mm² PVC insulated multi-strand flexible wire with an overall outside diameter of 1.5mm)		
Environmental			
Operating Temperature	-10°C to +60°C		
Outputs			
Status LED	Internally current limited 3VDC source – via 330R limiting resistor		
Limits	< 2mA (external resistor may be required)		
All other LEDs	4 (Internally current limited 3VDC source – via 100R limiting resistor)		
Limits	< 2mA (external resistor may be required)		
Communications (iMAC L	1)		
Hardware interface	2 wire (+/-18VDC I.S. via MLB barrier or +/-21VDC non-I.S. iMAC Fieldbus)		
Line Speed	300 - 1000 baud		
Bit protocol	iMAC proprietary		
L1 Isolation	5.3kV RMS		
L1 Line Loading (baud)	0.66mA (300) / 0.78mA (500) / 1.56mA (1000)		
Find Out More			

For more information on this product, contact Ampcontrol Customer Service on +61 1300 267 373 or <u>customerservice@ampcontrolgroup.com</u> or visit the Ampcontrol website: <u>www.ampcontrolgroup.com</u>



Equipment List		
Part Number	Description	
140213	MODULE IMAC LED4 IECEx	
142323	KIT IMAC DIN RAIL MOUNT	

### DISCLAIMER

While every effort has been made to ensure the accuracy of this document at the date of issue, Ampcontrol assumes no liability resulting from any omissions or errors in this document, and reserves the right to revise content at any time.